

# MATTINGLY, STANGER & MALUR, P.C.

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PATENT, TRADEMARK  
AND COPYRIGHT LAW  
FACSIMILE: (703) 684-1157

JC872 U.S. PTO  
09/864200  
05/25/01

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Date: May 25, 2001

Attorney Docket No. ASA-1006



24956

PATENT TRADEMARK OFFICE

To: Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir: Transmitted herewith for filing is the patent application of:

Inventor: K. WATANABE et al (see attached)

For:  
CLOCK PRODUCING CIRCUIT AND SEMICONDUCTOR  
INTEGRATED CIRCUIT FOR COMMUNICATION

Enclosed are:

☒ 7 Sheets of Drawings

This application is being filed without an executed Declaration.

Priority is claimed from Japanese Application No. 2000-160917  
filed May 30, 2000 ☒ A certified copy is attached herewith.

Copies of the disclosure documents listed on the attached PTO 1449 form and  
☐ discussed in the specification or ☐ attached Information Disclosure Statement.

A verified statement to establish small entity status under 37 CFR 1.9 and 1.27.

**JAPANESE LANGUAGE APPLICATION**

Specification: Abstract ☒ Description 18 pages; and 9 claim(s).

☐ Preliminary Amendment.

☐ Executed Declaration.

The filing fee is calculated as shown below:

Small Entry

Large Entry

For:	No. Filed	No. Extra
Basic Fee		
Total Claims	9 - 20 = *	0
Indep Claims	3 - 3 = *	0
<input type="checkbox"/> Multiple Dependent Claim (s)		

\* If difference is less than zero  
then enter '0' in second column

Rate	Fee
	\$ 355
x 9	\$
x 40	\$
+ 135	\$
Total	\$

Rate	Fee
	\$ 710
x 18	\$ 0
x 80	\$ 0
+ 270	\$ 0
Total	\$ 710

☒ A check in the amount of \$ 710.00 is enclosed for the filing fee.

☒ The Commissioner is hereby authorized to charge any additional fees that may be required to  
Deposit Account No. 50-1417.

Respectfully Submitted,

By John R. Mattingly  
Registration No. 30,293

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